Cooperative Data Sharing (CDS)

Overview

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Elepar's Three Layers





CDS: Cooperative Data Sharing

History: Early musings at OGI, prototype implemented and published at NASA Ames, now under development at Elepar

Approach: Determine common features of shared memory and message passing, build subroutine interface around those features, include other expected features (process control, active messages, conversion/marshalling).

Result: Compared to other communication layers (e.g. MPI, sockets, DSM), it is:

- Relatively simple/Small
- Expressive/Powerful
- Very portable to different uniprocessor & parallel architectures



CDS: Anatomy of a CDS Process



Comm Heap: Logically private heap. Data is optimized for communication. User is responsible for enlarging and/or shrinking.

Unix process.



CDS Basic Communication Operations



All ops that can block (i.e. bput, get, deq and read) take a time-out value, and also "i" versions (ibput, iget, ideq, and iread, respectively), resolved with a wait op.

"Copy" operation is virtual (i.e. usually copy on write), so these are usually just pointer ops. For portability, rgmod must be called before modifying any potentially-shared rgn



CDS: Logical View





CDS: Physical View on 3 Nodes



CDS: Other Functionality

Process Initiation/Active Messages ("Handlers")

High and low water marks can be set on each cell

A **"handler**" function can be chosed to be invoked each time that watermark is exceeded.

Copying/Marshalling/Conversion

Although process can access regions in comm heap directly, "copyfm", "copyto" routines exist to pack, unpack, and/or convert data as it is being moved to or from region, based on internallysupported conversion tables.



CDS Shared Mem & Msg Passing "Macros"

enqing reqion ~= releasing a lock, **deqing region** ~= acquiring a lock.

| "Macro" | Meaning | Translates Into | |
|---------|-------------------------|-----------------|--|
| acqwl | Acquire write lock | deq, rgmod | |
| rlswl | Release write lock | write, rgfree | |
| acqrl | Acquire read lock | read | |
| rlsrl | Release read lock | rgfree | |
| wl2rl | Write lock -> read lock | write | |

Msg passing includes copy to/from comm heap, can be optimized out.

| "Macro" | Meaning | Semantically identical to |
|---------|-------------------------|--------------------------------|
| send | Send message | rgalloc, copyto, enq, rgfree |
| recv | Receive message | deq, copyfm, rgfree |
| sendx | Destructive send | rgalloc, copyto, write, rgfree |
| recvx | Non-destructive receive | read, copyfm, rgfree |
| bsend | Synchro or ready send | rgalloc, copyto, bput, rgfree |

Corresponding "i" ops: iacqrl, iacqwl, irecv, irecvx, ibsend



Comparing CDS Featureset

| Features | C D S | D S M | M P I | S O C K | L I D A |
|--|-------------|-------------|-------------|------------------|------------------|
| Some data can be traded/shared in place (true 0 copy!) | | x | | | |
| Consumer can pull (get) data from passive producer | | x | 2 | | x |
| Consumer can prefetch/prepull data to hide latency | | ? | 2 | | |
| Producer can push (send) data to passive consumer | | | x | x | ? |
| Data can be queued at producer waiting for pull | | | x | x | ? |
| Pushed data can be made to overwrite previous value | | x | | | x |
| Producer can retain access rights to comm'd data | | | 2 | | x |
| Producer can relinq access rights to comm'd data | | x | x | | x |
| Dynamic memory allocation for shared memory | | ? | | | |
| Consumer can specify timeout for waiting | x | ? | | | |
| Supports heterogeneous platforms | | | x | | |
| Simplicity (~number of function + macro interfaces) | | 20 | !!! | 13 | 5 |



The CDS Interface

Managing comm heap and contexts/cells rgalloc rgmod rgfree rgsize rgrealloc addcntxt delcntxt grwcntxt **Communication Primitives** read deq benq enq write zap enqm writem iread ideq ibenq wait waitm ienqm benqm **Copying and Translation** copyto copyfm copytofm transtab **Composite functions (shared mem and msg passing)** bsend recvx send sendx sendm sendxm recv acqrl acqwl rlsrl rlswl wl2rl irecv ibsend irecvx iacqrl iacqwl **Process and thread control** enlist init myinfo hdlr prior